ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention	METHOD AND STRUCTURE FOR FORMING STRAINED Si FOR CMOS DEVICES

Submision Type :   Utility Patent Filing

Application Number:

                   10/605906

EFS ID: 50235

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	2905
IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fucntion call.
WGEN24	Filename= all XMLs having <invention-title> BusinessRule= Common elements across multiple XML documents within the submission package do not have synchronized data content. #Supporting Msg:<invention-title> not the same or having font fomat.

First Named Applicant: An Steegen

Attorney Docket Number: FIS920030236US1

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File Listing:

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us-assignment	FIS920030236US1-usassn.xml	2105	2003-11-05
us-assignment	us-assignment.xsl	8983	2003-11-05
us-assignment	us-assignment.dtd	10898	2003-11-05
us-assignment	FIS920030236Assignment1.tif	66140	2003-11-05
us-assignment	FIS920030236Assignment2.tif	7668	2003-11-05
us-request	FIS920030236US1-usrequ.xml	3058	2003-11-05
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application-body	fis920030236-trans.xml	39646	2003-11-05

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